


PALM INTRANET

Day : Friday
Date: 6/2/2006
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Inventor Name Search Result

Your Search was:

Last Name = STOICA

First Name = ADRIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07505597</u>	<u>5023892</u>	150	04/06/1990	SYSTEM FOR DETECTING AND CORRECTING SIGNAL DISTORTION	STOICA, ADRIAN
<u>09395235</u>	<u>6728666</u>	150	09/13/1999	AN EVOLVABLE CIRCUIT WITH TRANSISTOR-LEVEL RECONFIGURABILITY	STOICA, ADRIAN
<u>09442445</u>	Not Issued	66	11/17/1999	SYSTEM AND METHOD FOR PERFORMING PATTERN MATCHING IMAGE COMPRESSION	STOICA, ADRIAN
<u>09591386</u>	<u>6526556</u>	150	06/07/2000	EVOLUTIONARY TECHNIQUE FOR AUTOMATED SYNTHESIS OF ELECTRONIC CIRCUITS	STOICA, ADRIAN
<u>09793663</u>	<u>6922712</u>	150	02/26/2001	APPARATUS, METHODS, AND COMPUTER PROGRAM PRODUCTS FOR ACCURATELY DETERMINING THE COEFFICIENTS OF A FUNCTION	STOICA, ADRIAN
<u>09878784</u>	<u>6820104</u>	150	06/11/2001	APPARATUS, METHODS, AND COMPUTER PROGRAM PRODUCTS FOR REDUCING THE NUMBER OF COMPUTATIONS AND NUMBER OF REQUIRED STORED VALUES FOR INFORMATION PROCESSING METHODS	STOICA, ADRIAN
<u>10061066</u>	Not Issued	95	01/29/2002	EVOLUTIONARY TECHNIQUE FOR AUTOMATED SYNTHESIS OF ELECTRONIC CIRCUITS	STOICA, ADRIAN
<u>10740399</u>	Not Issued	30	12/22/2003	System for implementation of transforms	STOICA, ADRIAN
<u>10768754</u>	Not	30	01/26/2004	Evolutionary technique for	STOICA, ADRIAN

	Issued			automated synthesis of electronic circuits	
<u>10971568</u>	Not Issued	41	10/22/2004	Apparatus, methods, and computer program products for reducing the number of computations and number of required stored values for information processing methods	STOICA, ADRIAN
<u>11155312</u>	Not Issued	160	06/17/2005	Apparatus, methods, and computer program products for accurately determining the coefficients of a function	STOICA, ADRIAN
<u>11347676</u>	Not Issued	20	02/03/2006	Apparatus, methods, and computer program products for reducing the number of computations and number of required stored values for information processing methods	STOICA, ADRIAN
<u>60057379</u>	Not Issued	159	08/28/1997	RECONFIGURABLE TRANSISTOR ARRAY FOR EVOLVABLE HARDWARE	STOICA, ADRIAN
<u>60074903</u>	Not Issued	159	02/17/1998	BIO-MORPHIC EXPLORERS	STOICA, ADRIAN
<u>60185346</u>	Not Issued	159	02/26/2000	Methods and apparatus for processing and analyzing information	STOICA, ADRIAN
<u>60201609</u>	Not Issued	159	05/03/2000	Method and apparatus for processing and analyzing information	STOICA, ADRIAN
<u>60272377</u>	Not Issued	159	02/27/2001	Polymorphic electronics-a novel type of circuits with multiple functionality	STOICA, ADRIAN
<u>60274180</u>	Not Issued	159	03/09/2001	Evolvable antenna system	STOICA, ADRIAN
<u>60412004</u>	Not Issued	159	09/20/2002	On-sensor array signal processing	STOICA, ADRIAN
<u>60434804</u>	Not Issued	159	12/20/2002	Circuit for inverse transform and direct control of displays and other emitting/array systems	STOICA, ADRIAN
<u>60535223</u>	Not Issued	159	01/09/2004	4-Function logic gate controlled by analog voltage	STOICA, ADRIAN
<u>60642706</u>	Not Issued	159	01/10/2005	Morphable ripple counter controlled by analog voltage	STOICA, ADRIAN
<u>60643069</u>	Not Issued	159	01/11/2005	Circuit design technique for irreversible circuits based on hybrid	STOICA, ADRIAN

				reconfigurable cells	
60643448	Not Issued	159	01/13/2005	Method for embedding secret keys into electronic circuits	STOICA, ADRIAN
60679165	Not Issued	159	05/09/2005	3-Function logic gate controlled by analog voltage	STOICA, ADRIAN
60761999	Not Issued	20	01/24/2006	4-Function logic gate controlled by analog voltage	STOICA, ADRIAN
60762246	Not Issued	20	01/24/2006	Circuit design technique for irreversible circuits based on hybrid reconfigurable cells	STOICA, ADRIAN

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